05

Still yet another aspect of the present invention is the feedforward amplifier, further comprising:

Please replace the paragraph beginning at page 7, line 23:

alo

A further aspect of the present invention is the feedforward amplifier, further comprising:

Please replace the paragraph beginning at page 8, line 9:

01

A still further aspect of the present invention is the feedforward amplifier, wherein said distortion suppression power-combiner is a variable power-combiner that can have a tight coupling state and a loose coupling state, and

Please replace the paragraph beginning at page 8, line 21:

A80

A yet further aspect of the present invention is the feedforward amplifier, wherein said distortion suppression power-combiner is a variable power-combiner that can have a tight coupling state and a loose coupling state,

Please replace the paragraph beginning at page 9, line 7:

A still yet further aspect of the present invention is the feedforward amplifier, wherein, when said first signal level is not higher than a predetermined value, said control means performs such control that the output signal of said error amplifier can be output without passing through said distortion suppression power-combiner.

Please replace the paragraph beginning at page 9, line 14:

A10

An additional aspect of the present invention is the feedforward amplifier, wherein, when said first signal level is not lower than a predetermined value, said control means performs such control that the output signal of said error amplifier can be output without passing through said distortion suppression powercombiner.

Please replace the paragraph beginning at page 9, line 21:

AM

A still additional aspect of the present invention is the feedforward amplifier, further comprising:

Please replace the paragraph beginning at page 10, line 13:

012

A yet additional aspect of the present invention is the feedforward amplifier, further comprising:

Please replace the paragraph beginning at page 11, line 3:

A13

A still yet additional aspect of the present invention is the feedforward amplifier, further comprising:

Please replace the paragraph beginning at page 13, line 1:

AIY

A supplementary aspect of the present invention is the feedforward amplifier, further comprising:

Please replace the paragraph beginning at page 14, line 21:

AS

A still supplementary aspect of the present invention is the feedforward amplifier, further comprising:

Please replace the paragraph beginning at page 17, line 1:

A/6

A yet supplementary aspect of the present invention is the feedforward amplifier, further comprising:

Please replace the paragraph beginning at page 19, line 4:

AIZ

A still yet supplementary aspect of the present invention is the feedforward amplifier, wherein said first signal level detection means is provided in an upstream stage of said first power splitter, or between said first power splitter and said first vector adjustor, or between said first vector adjustor and said main amplifier, or between said first power splitter and said first delay circuit, or between said first delay circuit and said distortion detection power-combiner, or at the input of said baseband signal generating portion, or at the output of said baseband signal generating portion, or in said baseband signal generating potion, or at the input of said transmitting circuit, or at the output of said transmitting circuit, or in said transmitting circuit.

Please replace the paragraph beginning at page 19, line 18:

a/8

Another aspect of the present invention is the feedback amplifier, wherein said first signal level detection means is provided at the input of said receiving circuit, or at the output of said receiving circuit, or in said receiving circuit.

Please replace the paragraph beginning at page 19, line 24:

M9

Still another aspect of the present invention is the feedforward amplifier, wherein said second signal level detection means is provided in a downstream stage of said distortion suppression power-combiner, or between said second power splitter and said second delay circuit, or between said second delay circuit and said distortion suppression power-combiner.

Please replace the paragraph beginning at page 20, line 6:

A20

Yet still another aspect of the present invention is the feedforward amplifier,

Please replace the paragraph beginning at page 20, line 18:

M

Still yet another aspect of the present invention is the feedforward amplifier, wherein said second signal level detection means has a signal level detection power-splitter for splitting said output signal into two parts and detection means of detecting said signal level of one output signal of said signal level detection power-splitter, and

Please replace the paragraph beginning at page 21, line 3:

1822

A further aspect of the present invention is the feedforward amplifier, wherein the stopping of the operation of said error amplifier is to perform such control that the power supply for said error amplifier can be turned off and/or to perform such control that the output signal of said second vector adjustor can not be input by said error amplifier.

Please replace the paragraph beginning at page 21, line 11:

A23

A still further aspect of the present invention is the feedforward amplifier, wherein the stopping of the operation of said main amplifier is to perform such control that the power supply for said main amplifier can be turned off and/or to perform such control that the output signal of said first vector adjustor can not be input by said main amplifier.

Please replace the paragraph beginning at page 21, line 19:

A24

A yet further aspect of the present invention is the feedforward amplifier, wherein the stopping of the operation of said second error amplifier is to perform such control that the power supply for said second error amplifier can be turned off and/or to perform such control that the output signal of said third vector adjustor can not be input by said secondary error amplifier.

Please replace the paragraph beginning at page 22, line 3:

A25

A still yet further aspect of the present invention is a communication equipment comprising:

Please replace the paragraph beginning at page 22, line 7:

A76

A Bas and

h-t

a transmitting circuit for outputting a transmitting signal from said baseband signal generated, wherein the feedforward amplifier is used for said transmitting circuit.

Respectfully Submitted,

Allan Rather, Reg. No. 19,717 Attorney for Applicants

AR/lm

Enclosure: Version With Markings Showing Changes Made

Dated: September 21, 2001

Suite 301, One Westlakes, Berwyn

P.O. Box 980

Valley Forge, PA 19482-0980

(610) 407-0700

The Assistant Commissioner for Patents is hereby authorized to charge payment to Deposit Account No. 18-0350 of any fees associated with this communication.

EXPRESS MAIL Mailing Label Number: EL 741593015US
Date of Deposit: September 21, 2001

I hereby certify that this paper and fee are being deposited, under 37 C.F.R. § 1.10 and with sufficient postage, using the "Express Mail Post Office to Addressee" service of the United States Postal Service on the date indicated above and that the deposit is addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231.

Kathleen Libby